

REMARKS

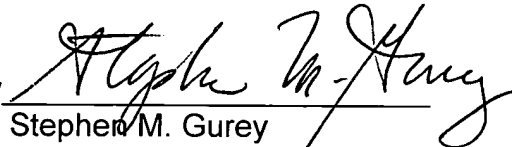
The present application still stands with claims 1, 2, and 4-6 rejected under 35 U.S.C. §101 as being inoperative. This rejection is respectfully rebutted.

Enclosed herewith is a second sworn declaration of the applicant. This declaration includes six numbered sections. In this declaration, the points out to the Examiner, in particularly in numbered paragraph 3, that the invention is operative provided an assumption is made that the respective most significant bits of two binary numbers represent the same value, e.g., when adding the two numbers. In the declaration, the applicant indicates that this assumption is well known in the relevant technical field, for example, after analog to digital conversion of an analog signal that takes a restricted range of levels. This assumption contrasts with the Examiner's assumption from classical binary mathematics theory that numbers are integers and so the least significant bits represent the same values.

Based upon the foregoing reasons, applicant submits that each of the claims presently in the application is believed to be in a condition for allowance. Passage to issue of the subject application is therefore respectfully requested. Should the Examiner feel that the present application is not yet in a condition for allowance and that a telephone or personal interview would be helpful, he is invited to contact applicants' undersigned attorney at **973, 386 8252**.

Respectfully submitted,

Martin Lehnert

By 
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Attached Declaration of Applicant